

# Indian Institute of Technology Indore

Simrol Campus, Khandwa Road, Indore – 453552

Discipline of Computer Science and Engineering

Advertisement for Admission to Ph.D. Program for Autumn Semester of AY 2017-18

IITI/Acad/PhD Admissions/Adv/CSE/2017-18/Autumn

April 17, 2017

IIT Indore invites applications from highly motivated and research-oriented students for admission to its **PhD Program** in the discipline of Computer Science and Engineering for the **Autumn Semester of AY 2017-18** as per the below-mentioned categories of admission and time schedule. Candidates are strongly advised to visit the profiles of the faculty members at <http://cse.iiti.ac.in/faculty.html> before applying for the PhD Program.

**Categories of Admission:** PhD program is Full-Time (except DF category) and is offered under the following categories:

- **FA:** Fellowship Awardees from the agencies such as CSIR, UGC, etc. or the JRF/SRF project staff working in a Sponsored Research Project under a faculty of IIT Indore being PI of the project. The Scholarship will as per the rules/guidelines of the concerned Funding Agency.
- **TA:** Teaching Assistantship with scholarship as per MHRD guidelines.
- **SW:** Sponsored from a reputed Industrial or Research organization WITHOUT any scholarship from the Institute.
- **DF:** Serving personnel in the defense forces WITHOUT any scholarship from the Institute (can be Part-Time also).

**Time Schedule of PhD admission:**

Last date of Online Application through <a href="http://academic.iiti.ac.in:8080/nregistration.jsp">http://academic.iiti.ac.in:8080/nregistration.jsp</a>	June 17, 2017 (Saturday)
Date of Written Test and/or Interview	June 19, 2017 (Monday)

### Eligibility/Shortlisting Criterion (Qualifying Degree and Exam)

1. Minimum first class\* Master degree in the relevant discipline of the Engineering/Technology, **OR**
2. Minimum first class\* Bachelor degree in the relevant engineering/ technology discipline from a reputed Institute with a **valid GATE score**, **OR**
3. BTech degree from an Indian Institute of Technology (IIT) with a minimum CPI of 8.0 **OR**
4. Minimum first class\* Master degree in the relevant discipline of Science with **valid GATE** qualification **OR UGC/CSIR-JRF** qualification **OR** equivalent fellowship

\* The first class is defined as

- (i) 60 % marks for GEN/OBC (55% for SC/ST) category in aggregate or as specified by the university/institute, **OR**
- (ii) CPI/CGPA of 6.0 for GEN/OBC (5.5 for SC/ST) category on the scale of 10 with corresponding proportional requirements when the scales are other than on 10 (for example 4.8 for GEN/OBC category (4.4 for SC/ST) on a scale of 8), **OR**
- (iii) A first class as specified by the University/Institute awarding the degree.

### Written Test/Interview

Ph.D. written test/interview would be conducted on **June 19, 2017 (Monday)** at 9:00 AM at **POD Building**, Simrol Campus, Khandwa Road, IIT Indore. Candidates fulfilling the eligibility/shortlisting criterion mentioned above should appear for the written test and/or interview. Interview may be scheduled on the next day *i.e.* on **June 20, 2017 (Tuesday)** also. **Kindly note that no separate email will be sent to the candidates regarding test/interview.** For more details and latest updates, kindly visit <http://cse.iiti.ac.in/>.

### Areas of Specialization

For more details; visit faculty profiles at <http://cse.iiti.ac.in/faculty.html>

<b>Dr. Abhishek Srivastava</b>	Service-Oriented Systems, Web Services, Internet of Things, Cyber-Physical Systems For information visit: <a href="http://iiti.ac.in/people/~asrivastava/">http://iiti.ac.in/people/~asrivastava/</a>
<b>Dr. Anirban Sengupta</b>	Hardware Security, Hardware Watermarking, Hardware Trojan Security, Digital Forensics, High Level Synthesis in CAD VLSI, Transient Fault detection in CAD VLSI, Optimization of Control Intensive Applications, for Hardware Accelerator Design in CAD VLSI, Loop unrolling optimization in CAD VLSI. For information visit: <a href="http://iiti.ac.in/people/~asengupt/">http://iiti.ac.in/people/~asengupt/</a>
<b>Dr. Aruna Tiwari</b>	Soft Computing, Machine Learning Algorithms: Neural Network Learning Algorithms, Fuzzy Clustering, Evolutionary Algorithms, Data mining, Design of scalable learning algorithms for Big data handling, Embedded Intelligence. For information visit: <a href="http://iiti.ac.in/people/~artiwari/">http://iiti.ac.in/people/~artiwari/</a>
<b>Dr. Bodhisatwa Mazumdar</b>	Hardware Security, Side channel analysis attacks on cryptographic implementations, Security aspects in emerging VLSI technologies. For information visit: <a href="http://iiti.ac.in/people/~bodhisatwa/">http://iiti.ac.in/people/~bodhisatwa/</a>
<b>Dr. Gourinath Banda</b>	Embedded Systems' Formal Verification, Internet-of-Things (IoT), IoT Protocol Design, End-2-End IoT Simulation, Wireless Sensor Networks, Green Computing, Cyber-physical Systems (CPS), CPS: Drones, AUV, UAV. For information visit: <a href="http://iiti.ac.in/people/~gourinath/">http://iiti.ac.in/people/~gourinath/</a>
<b>Dr. Neminath Hubballi</b>	Computer Networks, Network Security, System Security, Cloud Security. For information visit: <a href="http://iiti.ac.in/people/~neminath/">http://iiti.ac.in/people/~neminath/</a>
<b>Dr. Somnath Dey</b>	Biometrics, Image Processing, Pattern Recognition, Machine Vision, Human Computer interaction For information visit: <a href="http://iiti.ac.in/people/~somnathd/">http://iiti.ac.in/people/~somnathd/</a>
<b>Dr. Surya Prakash</b>	Biometrics, Pattern Recognition, Computer Vision, Image Processing, Big Data. For information visit: <a href="http://iiti.ac.in/people/~surya/">http://iiti.ac.in/people/~surya/</a>

**Application Fee: Rs. 100/- to be paid preferably through a Demand Draft (DD) drawn in favour of "Registrar, IIT Indore" and payable at Indore.**

**Application Procedure:**

- Candidates must apply ONLINE through our website (<http://academic.iiti.ac.in:8080/nregistration.jsp>). This will generate a unique application number for each applicant.
- After Submitting the application online, the same is to be downloaded and the signed hard-copy along with Demand Draft (or Proof of online fee payment), recent photograph, self-attested relevant certificates (for SW candidates - NOC, experience certificate, last 3 months salary slips and Employer's PAN card) must be brought on the day of test/interview. **Kindly note that there is no need to post the hard copy of the application or any other document.**
- Candidate MUST write his/her name, name of the discipline and the Application Number on the reverse side of the Demand Draft.

**IMPORTANT NOTES:**

- (A) Selected candidates may be considered for admission under the PhD scheme of Department of Electronics and Information Technology (DeitY) (now named as "Visvesvaraya PhD Scheme for Electronics and IT") depending upon the availability of the seats.
- (B) **Please note that there is no need to post the application form, bank draft and any other documents. Prospective candidates MUST bring application form and relevant document at the time of test/interview. Please see "Application Procedure" above for instructions.**

For any further queries please contact:

PhD Admission Cell,

Indian Institute of Technology Indore,

Indore- 453552, Indore, Madhya Pradesh, India.

Email: [cseadmission@iiti.ac.in](mailto:cseadmission@iiti.ac.in), [phdadmission@iiti.ac.in](mailto:phdadmission@iiti.ac.in), [dracademic@iiti.ac.in](mailto:dracademic@iiti.ac.in).